Xen**O**pt

XS311-02Lx

1.25 Gbps SFP Optical Transceiver, 1310 nm, MMF, 2 km Reach

Applications

- Gigabit Ethernet
- Fiber Channel
- Switch to Switch interface
- Switched backplane applications
- Router/Server interface
- Other optical transmission systems
- Out of band monitoring, fiber integrity and performance monitoring

Features

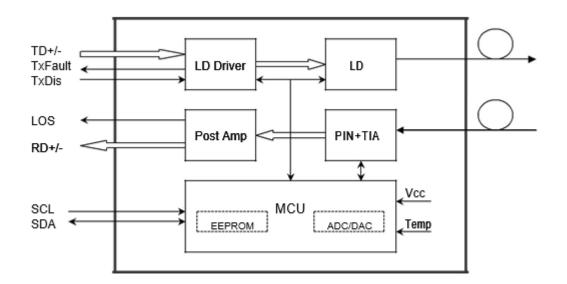
- Data-rate of 1.25Gbps operation
- 1310nm FP LD laser and PIN photodetector
- Compliant with SFP MSA and SFF-8472 with duplex LC receptacle
- Digital Diagnostic Monitoring: Internal Calibration or External Calibration
- 2km transmission with 50/125 μ m MMF
- 1km transmission with 62.5/125 μm MMF
- Compatible with RoHS
- +3.3V single power supply
- Operating case temperature: Standard: 0 to +70°C
 Industrial: -40 to +85°C

Description:

The SFP transceivers are high performance, cost effective modules supporting data-rate of 1.25Gbps and 2KM transmission distance with MMF.

The transceiver consists of three sections: a 1310nm FP LD laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement (MSA) and SFF-8472. For further information, please refer to SFP MSA.



Absolute Maximum Ratings

Table 1 - Absolute Maximum Ratings

Parameter	Symbol	Min	Мах	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Table 2 - Recommended Operating Conditions

Parameter		Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Standard		0		+70	*0
		Тс	-20		+85	°C
	industrial		-40		+85	°C
Power Supply Voltage		Vcc	3.13	3.3	3.47	V
Power Supply Current		lcc			300	mA
Data Rate				1.25		Gbps

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Optical and Electrical Characteristics

Table 3 - Optical and Electrical Characteristics

Param	eter	Symbol	Min	Typical	Max	Unit	Notes
			Transmitt	er			
Centre W	/avelength	λς		1310		nm	
Spectral V	/idth (RMS)	Δλ			0.85	nm	
Average O	utput Power	Pout	-5		0	dBm	1
Extincti	on Ratio	ER	9			dB	
Optical Rise/Fall	Time (20%~80%)	tr/tf			0.26	ns	
Data Input Sw	ving Differential	Vin	400		1800	mV	2
Input Differen	tial Impedance	Zin	90	100	110	Ω	
	Disable		2.0		Vcc	V	
TX Disable	Enable		0		0.8	V	
	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
			Receive	r			
Centre W	/avelength	λc		1310		nm	
Receiver	Receiver Sensitivity				-20	dBm	3
Receiver	Overload		0			dBm	3
LOS D	e-Assert	LOSD			-21	dBm	
LOS Assert		LOSA	-35			dBm	
LOS Hysteresis			1		4	dB	
Data Output S	wing Differential	Vout	400		1800	mV	4
	<u></u>	High	2.0		Vcc	V	
	OS	Low			0.8	V	

Notes:

1. The optical power is launched into MMF.

2. PECL input, internally AC-coupled and terminated.
 3. Measured with a PRBS 2⁷-1 test pattern @1250Mbps, BER ≤1×10⁻¹².

4. Internally AC-coupled.

Timing and Electrical

Table 4 - Timing and Electrical

Parameter	Symbol	Min	Typical	Мах	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock			400	KHz
MOD_DEF (0:2)-High	Vн	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	V

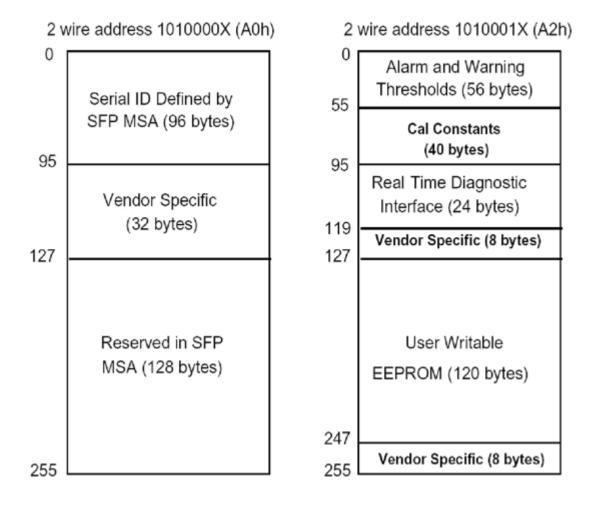


Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

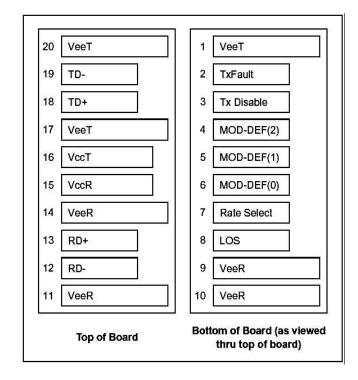
The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.



Pin Definitions

Pin Diagram



Pin Descriptions

Pin	Signal Name	Description	Plug Seq.	Notes
1	Veet	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	MOD_DEF(2)	SDA Serial Data Signal	3	Note 3
5	MOD_DEF(1)	SCL Serial Clock Signal	3	Note 3
6	MOD_DEF(0)	TTL Low	3	Note 3
7	Rate Select	Not Connected	3	
8	LOS	Loss of Signal	3	Note 4
9	Veer	Receiver ground	1	
10	Veer	Receiver ground	1	
11	Veer	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 5
13	RD+	Received Data Out	3	Note 5
14	Veer	Receiver ground	1	
15	Vccr	Receiver Power Supply	2	
16	Vсст	Transmitter Power Supply	2	
17	Veet	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 6
19	TD-	Inv. Transmit Data In	3	Note 6
20	Veet	Transmitter Ground	1	

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Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

- TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7k~10kΩ resistor. Its states are:

Low (0 to 0.8V):	Transmitter on
(>0.8V, < 2.0V):	Undefined
High (2.0 to 3.465V):	Transmitter Disabled
Open:	Transmitter Disabled

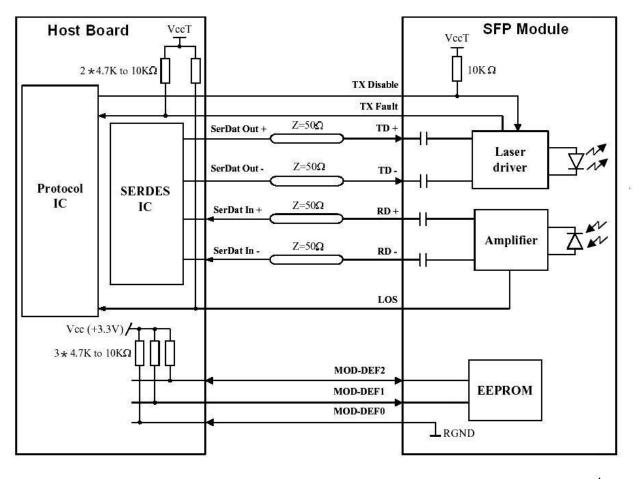
3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7k~10kΩ resistor on the host board. The pull-up voltage shall be VccT or VccR. Mod-Def 0 is grounded by the module to indicate that the module is present Mod-Def 1 is the clock line of two wire serial interface for serial ID

Mod-Def 2 is the data line of two wire serial interface for serial ID

- 4) LOS is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor. Pull up voltage between 2.0V and Vcc+0.3V. Logic 1 indicates loss of signal; Logic 0 indicates normal operation. In the low state, the output will be pulled to less than 0.8V.
- 5) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 6) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

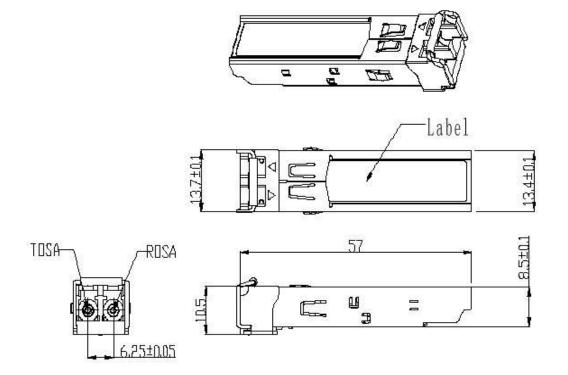
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Recommended Interface Circuit



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Mechanical Dimensions



Ordering information

Part Number	Product Description			
XSM311-02LN	SFP 1.25G 1310nm MMF 2km	0ºC ~ +70ºC		
XSM311-02LY	SFP 1.25G 1310nm MMF 2km DDM	0ºC ~ +70ºC		
XSM311-02LL	SFP 1.25G 1310nm MMF 2km	-40ºC ~ +85ºC		
XSM311-02LM	SFP 1.25G 1310nm MMF 2km DDM	-40ºC ~ +85ºC		

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Notes:

¹ For accurate order specification please contact Xenopt reseller before placing an order. The content of this document is subject to change without notice. Xenopt does not guarantee errorless or outdated information. Please specify any compatibility requirements at time of ordering. Standard MSA compatible pluggable components may not work or some function of these components may not be available in devices that require customized compatible devices. Pluggable components compatible with one type of communications equipment may not work in other type of communications equipment.

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